

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0044	
				Application No.: 10/813,837	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicant(s): Garlapati et al.	
				Filing Date: March 31, 2004	
				Group Art Unit: 2819	
				Date Submitted: July 14, 2004	

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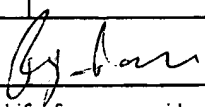
U.S. Patent Documents				
*Examiner Initial	Document Number	Date	Name	
Z	AA 4,588,941	May 13, 1986	Kerth et al.	<div style="border-left: 2px solid black; border-right: 2px solid black; height: 100px; margin: 0 auto;"></div>
Z	AB 4,857,823	Aug. 15, 1989	Bitting	
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				Yes	No
Z	AL JP 1292411 (English abstract)	Nov. 24, 1989	Japan		

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Z	AM	Allen et al., "Current and Voltage References," <u>CMOS Analog Circuit Design</u> , 1987, pp. 240-252.
Z	AN	Allen et al., "CMOS Analog Circuits and Systems," <u>CMOS Analog Circuit Design</u> , 1987, pp. 589-599.
Z	AO	Banba et al., "A CMOS Bandgap Reference Circuit with Sub-I-V Operation," <u>IEEE Journal of Solid-State Circuits</u> , Vol. 34, No. 5, May, 1999, pp. 670-674.

Examiner		Date Considered	3/1/06
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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A	AU	Gray et al., "Band-Gap-Referenced Biasing Circuits," <u>Analysis and Design of Analog Integrated Circuits</u> , Third Edition, 1993, pp. 338-346.				
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